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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,741	10/27/2003	Jian-Shen Yu	YUJI3001/EM	8878
23364 7590 05/31/2007 BACON & THOMAS, PLLC 625 SLATERS LANE FOURTH FLOOR ALEXANDRIA, VA 22314			EXAMINER SHANKAR, VIJAY	
			ART UNIT 2629	PAPER NUMBER
			MAIL DATE 05/31/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/692,741

Applicant(s)

YU, JIAN-SHEN

Examiner

VIJAY SHANKAR

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 March 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-8 are rejected under 35 U.S.C. 102(b) as being anticipated by Kabuto et al (5,151,689).

Regarding Claim 1, Kabuto et al teaches a driving circuit for a flat display panel (Figures 1-31; Column 10, line 19- Col.18, line 14), comprising: a plurality of video signal lines for providing video signals (figs.15-16; Col.10, line 19- Col.11, line 68); at least one buffer unit for inverting a scanning signal (24 in figs.1,16; Col.3, line 30- Col.4, line 20); and a plurality of switch units disposed between the plurality of video signal lines (23 in figs.1,16; Col.3, line 30- Col.4, line 20); wherein each of the plurality of switch units is connected to at least one signal line to receive a video signal and is connected to the buffer unit to receive

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the scanning signal inverted by the buffer unit (Fig.19, Col.11, line 21-56), and wherein the scanning signal controls output of the video signals by the plurality of switch units to a display area of the flat display panel. (Figs.1,15-20; Col.3, line 30- Col.6, line 68; Col.10, line 19- Col.11, line 56).

Regarding Claim 2, Kabuto et al teaches a driving circuit wherein the plurality of switch units and the display area of the panel are spaced apart with at least one video signal line. (Figs.1,15-20; Col.10, line 19- Col.11, line 56).

Regarding Claims 3-4,6-7, Kabuto et al teaches a driving circuit wherein the buffer unit for inverting a scanning signal is an inverting circuit receiving a timing signal which is then inverted to output at least one scanning signal, and the at least one scanning signal is an inversed signal of the timing signal; one signal line is disposed between the plurality of switch units and the buffer unit for inverting a scanning signal, and the plurality of signal lines are disposed between the switch units and the active area. (Figs.1,15-20; Col.10, line 19- Col.11, line 56).

Regarding Claim 5, Kabuto et al teaches a driving circuit wherein the plurality of switch units are thin-film transistors.(Element T1, T2, T3, T4, T5 in fig.16).

Regarding Claim 8, Kabuto et al teaches a driving circuit wherein the flat display panel comprises a liquid crystal display panel (Fig.21-27; Col.12, lines 17-65)..

Response to Arguments

4. Applicant's arguments filed 3/1/07 have been fully considered but they are not persuasive.

Applicant argues that Kabuto et al does not teach a driving circuit comprising wherein each of the plurality of switch units is connected to at least one signal line to receive a video signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit.

However, Kabuto et al teaches a driving circuit comprising wherein each of the plurality of switch units is connected to at least one signal line to receive a video

signal and is connected to the buffer unit to receive the scanning signal inverted by the buffer unit (Fig.19, Col.11, line 21-56).

5. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

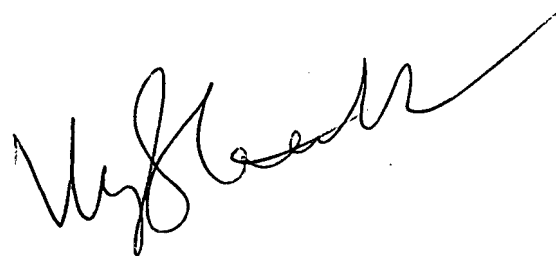
A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

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6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629

VS